

Amendment to RFP Document for Development of Computerized Integrated Checkposts on  
DBOT basis

S. No.	Clause No	Existing Clause	Amended Clause
1	58.1 57.13	Rack Mountable Not more than 4U  All Items are to be Rack Mounted in Industry Standard 42U Racks	Deleted
2	58.2	Four 64-bit Intel Xeon processor MP with clock speed up to 2.93 GHz Quad-Core, 1066 MHz FSB, expandable to 16 CPU's supporting Extended Memory 64 Technology.	Four 64-bit Intel Xeon processor MP with clock speed 2.26 GHz or above, 1066 MHz FSB expandable to 8 CPU's supporting Extended Memory 64 Technology.
3	57.5	L2 Cache - 4 MB per Processor Core, L3 Cache - 32 MB per Processor Core Pair	Vendor should provide maximum available cache in their product line
4	58.11	Two / 1440 watt (240 VAC) / rear access / hot-swap / redundant / worldwide / voltage-sensing / auto-restart / PFA	Vendor should quote power supply that is sufficient to support the quoted configuration. Power supply should be redundant.
5	57.16	Memory Protection - Chipkill and ECC Double Bit Steering First Failure Data Capture ECC L2 and L3 Cache	Please quote with equivalent or better functionality
6	60.1	The Tape system should be configured with two LTO Gen4 Fibre drives and 30slots and should be scalable to at least 18 drives and over 360 slots.	The Tape system should be configured with two LTO Gen4 Fibre drives and 30slots and should be scalable to at least 16 drives and over 360 slots.
7	49.1	Should be compliant with open specifications like J2EE™ and J2SE™.	Should be compliant with open specifications like J2EE and J2SE or .NET etc.
8	51.1.2	The proposed solution should support an open extensible environment. The solution should be based on industry standards such as J2EE, XML API etc. for custom application development.	The proposed solution supports an open extensible environment. The solution should be based on industry standards such as J2EE,

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			.NET, XML, API etc. for custom application development.
9	53, 54	Specifications for Database and Specifications for Data Warehouse	<p>Stands replaced by:</p> <p>Bidders may choose from world leading database such as Oracle/Microsoft SQL Server/IBM DB2 Enterprise Edition.</p> <p>However, it is to be ensured by the Concessionaire that the system has to be maintained and operated in accordance with Maintenance Requirements and Service Level Indicators defined in the RFP. In case of any deficiency in this regard, the Concessionaire shall deemed to be in breach of the Concession Agreement and GoP has a right to take remedial measures or recover damages in accordance with the provisions of the RFP and Concession Agreement.</p>
10	57.1	64 Bit server with latest generation of RISC/EPIC Processor with 1.6 or higher clock speed available at the time of bidding in the proposed server. Offered Servers should have Binary compatibility with the earlier and later generations of processors and operating System.	<p>Bidders may choose operating system such as Windows/Unix/Linux Enterprise Edition and Processor such as RISC/EPIC/X64. Also the solution should be based on Open Standards &amp; should be Interoperable.</p> <p>Other terms of the RFP and concession agreement will accordingly stand modified.</p> <p>However, it is to be ensured by</p>

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			<p>the Concessionaire that the system has to be maintained and operated in accordance with Maintenance Requirements and Service Level Indicators defined in the RFP. In case of any deficiency in this regard, the Concessionaire shall be deemed to be in breach of the Concession Agreement and GOP has a right to take remedial measures or recover damages in accordance with the provisions of the RFP and Concession Agreement.</p>
11	57.10	Atleast 40 PCI Slots upgradeable 70.	<p>Minimum 10 PCI Slots</p> <p>However, as already specified in the RFP and Concession Agreement, it is the responsibility of the Concessionaire to ensure that any upgradation requirement in the future will have to be met fully so that Maintenance Requirements and Service Level Indicators are complied with.</p>